

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1 - 9. (Canceled)

10. (Currently Amended) A data driving apparatus for driving an organic electro luminescence display (OELD) panel, comprising:

a data driver that outputs data signals formed using a substantially uniform current;

a data signal controller circuit connected to the data driver for charging a substantially uniform current corresponding to the outputted data signals and for applying the outputted charged current corresponding to the outputted data signals, wherein light is emittable by the OELD panel in the presence of the substantially uniform current; and

a plurality of data lines coupled between the data signal controller circuit and the OELD panel,

wherein the data signal controller circuit includes:

a plurality of constant current supply switching devices having gate terminals connected to a cell drive voltage source for applying the substantially uniform current to the data lines;

a first data signal controller circuit for storing a voltage corresponding to the data signals outputted by the data driver during application of a first scan signal;

a second data signal controller circuit for storing a voltage corresponding to a data signal outputted from the first data signal controller circuit and for applying the stored voltage to the data lines between application of the first scan signal and a subsequent application of a second scan signal;

a first switch connected between the data driver and the first data signal controller circuit for providing a current path between the data driver and the first data signal controller circuit;

a second switch connected between the first data signal controller circuit and the second data signal controller circuit for providing a current path between the first data signal controller

circuit and the second data signal controller circuit;

a drive signal supplier including a shift register for driving the first data signal controller circuit and the first switch during application of the first scan signal and a line pass controller for driving the second data signal controller circuit and the second switch between application of the first scan signal and application of the second scan signal for driving the first and second data signal controller circuit,

wherein the first data signal controller circuit includes: a fifth switching device connected between the cell drive voltage source and the second switch; a first capacitor connected between a gate terminal of the fifth switching device and the cell drive voltage source; and a third switch connected between the gate terminal of the fifth switching device and the second switch, wherein the third switch is controllable by the shift register,

and the second data signal controller circuit includes:

a sixth switching device connected between the second switch and a ground voltage source; a second capacitor connected between a gate terminal of the sixth switching device and the ground voltage source; and

a fourth switch connected between the gate terminal of the sixth switching device and the second switch, wherein the fourth switch is controllable by the line pass controller.

11. (Canceled)
12. (Currently Amended) The data driving apparatus according to claim [[11]] 10, wherein the first switch is controllable by the shift register.
13. (Currently Amended) The data driving apparatus according to claim [[11]] 10, wherein the second switch is controllable by the line pass controller.
14. (Currently Amended) The data driving apparatus according to claim [[11]] 10, wherein at least one of the switching devices is provided as a p-type metal oxide semiconductor field effect transistor (MOSFET).
15. (Currently Amended) The data driving apparatus according to claim [[11]] 10, wherein at least one of the switching devices is provided as an n-type metal oxide semiconductor field effect transistor (MOSFET).

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16 - 33. (Canceled)